

MP0018.C1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Continuation of Application No.: **09/699,772**
Filing Date: February 19, 2004
Applicant(s): Sehat SUTARDJA
Group Art Unit: Unknown
Examiner: Unknown
Title: A LOW PHASE NOISE MOS LC
OSCILLATOR

INFORMATION DISCLOSURE STATEMENT
(SUBMISSION CONCURRENT WITH THE
FILING OF A NEW PATENT APPLICATION)

MAIL STOP PATENT APPLICATION

February 19, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications, or other information submitted for consideration by the Office are listed on PTO-1449, attached hereto.

II. COPIES

☐ Submitted herewith is a legible copy of (i) each U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.

- ☐ Because the present application was/is being filed after June 30, 2003, no copies of the U.S. patents or U.S. patent application publications which are listed on the attached Form 1449 are enclosed pursuant to the waiver of 37 C.F.R. § 1.98(a)(2)(i). Any foreign patent documents or non-patent literature listed on the attached Form 1449 are enclosed herewith.
- ☒ This application is a continuation of U.S. Patent Application No. 09/699,772. All of the documents listed on the copies of the PTO-1449s and PTO-892 are not enclosed because they were cited in U.S. Patent Application No. 09/699,772. If copies are needed, please contact the undersigned.

III. CONCISE EXPLANATION OF THE RELEVANCE

(check at least one box)

a. ☐ **DOCUMENTS IN THE ENGLISH LANGUAGE**

The attached patents, publications, or other information in the English language do not require a statement of relevancy.

b. ☐ **DOCUMENTS NOT IN THE ENGLISH LANGUAGE**

A concise explanation of the relevance of all patents, publications, or other information listed that is not in the English language is as follows:

c. ☐ **OTHER**

FEES

This Information Disclosure Statement is being filed concurrently with the filing of a new patent application; therefore, no fee is required.

If The Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 08-0750.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any

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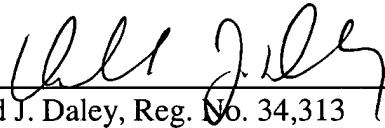
PATENT

additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKEY & PIERCE, P.L.C

By:


Donald J. Daley, Reg. No. 34,313

DJD/bof

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Reston, Virginia 20195
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Enclosures: ☒ Form PTO-1449(s)
☐ Documents
☐ Fee

Please address all correspondence to:

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<p>FORM HDP-1449 (Based on Form PTO-1449)</p> <p>PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p> <p>Sheet 1 of 2</p>	ATTORNEY DOCKET No.	SERIAL No.
	MP0018.C1	NEW
	APPLICANT(S)	
	Schat SUTARDJA	
	FILING DATE	GROUP
	February 19, 2004	Unknown

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
		US 5,920,235	07/1999	BEARDS et al.		
		US 5,850,163	12/15/1998	DROST et al.		
		US 5,959,504	09/28/1999	WANG		
		US 5,714,911	02/03/1998	GILBERT		
		US 5,949,295	09/07/1999	SCHMIDT		

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes No	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
		"10MHz CMOS OTA-C Voltage-Controlled Quadrature Oscillator", Linares-Barranco et al., IEEE Electronics Letter, June 1989, pp. 765-767, Vol. 25, Issue No. 12.
		RC Sequence Asymmetric Polyphase Networks for RF Intergrate Transceivers", Galal et al., Transactions On Circuits and Systems-11: Analog and Digital Signal Processing, Jan. 2000, Vol. 47, Issue No. 1, pp. 18-27.
		Phase Noise In CMOS Differential LC Oscillators", Hajimiri, A. et al., Digest of Technical Papers - 1998 Symposium on VLSI Circuits, 1998, pp. 48-51.
		A Low-Noise, 900 MHz VCO in 0.6-um CMOS" (Park et al.), IEEE Journal of Solid-State Circuits, Vol. 34, pp. 586-591, May 1999, Issue No. 5
		A 1.8 Ghz CMOS Voltage-Controlled Oscillator", B. Razavi, Digest of Technical Papers., 43 rd ISSCC, 1997, pp. 388-389, and Fig. 4.

Examiner:	Date Considered:
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EXAMINER: Please initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM HDP-1449 (Based on Form PTO-1449) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) Sheet 2 of 2	ATTORNEY DOCKET NO.	SERIAL NO.
	MP0018.C1	NEW
	APPLICANT(S)	
	Sehat SUTARDJA	
	FILING DATE	GROUP
February 19, 2004	Unknown	

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
		US 6,008,701	12/28/1999	GILBERT		
		US 5,475,345	12/12/1995	GABARA		

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation	
						Yes	No

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
		"Design Issues in CMOS Differential LC Oscillators", Hajimiri, A. et al. IEEE Journal of Solid-State Circuits, pp. 717-724, May 1999, Vol. 34, Issue No. 5

Examiner:	Date Considered:
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